WHAT IS CLAIMED:

1. A system comprising:

a radio receiver;

switched mode circuitry operating at a selected switching frequency; and

circuitry for setting said switching frequency of said switched mode circuitry as a function of a frequency of a signal being received by said radio receiver.

2. The system of Claim 1 wherein said switched mode circuitry comprises a switching power supply.

3. The system of Claim 1 wherein said switched mode circuitry comprises a Class D amplifier.

4. The system of Claim 1 wherein said circuitry for setting said switching frequency of said switched mode circuitry comprises:

a plurality of crystals of differing resonance frequencies;

a crystal oscillator for generating said switching frequency from a selected one of said crystals; and

control circuitry for selecting said selected one of said crystals.

5. The system of Claim 1 wherein said circuitry for setting said switching frequency of said switched mode circuitry comprises:

a signal generator for generating a base frequency;

a programmable divider for dividing said base frequency by a selected divisor to generate said switching frequency;

control circuitry for selecting said divisor.

6. The system of Claim 1 wherein said circuitry for setting said switching frequency includes a microcontroller operable to select said switching frequency in response to selection of a reception frequency band by user input.

7. The system of Claim 1 wherein said circuitry for setting said switching frequency detects said frequency of said signal received by said radio receiver by measuring a local oscillator frequency.

8. The system of Claim 1 wherein said switching frequency is selected such that at least one harmonic of said switching frequency lies outside a frequency band including said signal being received by said radio receiver.

9. An amplifier for use in a system including a radio receiver comprising:

an output transistor for driving an output; and

pulse width modulation circuitry for generating a pulse width modulated signal in response to an input signal for switching the conduction state of said output transistor, a frequency of said pulse width modulated signal selected as a function of a frequency of a signal received by the radio receiver.

10. The amplifier of Claim 9 wherein said pulse width modulation circuitry comprises:

a crystal oscillator for generating an oscillator signal of a selected base frequency from a selected one of a plurality of crystals;

a microcontroller for selecting said selected one of said crystals as a function of said frequency of said signal received by said radio receiver; and

circuitry for converting said oscillator signal into said pulse width modulated signal.

11. The amplifier of Claim 10 wherein said circuitry for converting comprises a ramp generator for generating a ramped signal in response to an output of said oscillator and a comparator for comparing the input signal with an output of said ramp generator.

12. The amplifier of Claim 9 wherein said pulse width modulation circuitry comprises:

a signal generator for generating a base signal of a selected base frequency;

a divider for dividing said base frequency by a selected divisor to generate a signal at said frequency of said pulse width modulated signal;

a microcontroller for selecting said divisor as a function of said frequency of said signal received by said radio receiver; and

circuitry for converting said signal at said frequency of said pulse width modulated signal into said pulse width modulated signal.

13. The amplifier of Claim 12 wherein said signal generator comprises a crystal oscillator.

14. The amplifier of Claim 9 wherein said output transistor comprises a metal oxide semiconductor field effect transistor.

15. The amplifier of Claim 9 wherein said frequency of said pulse width modulated signal is selected such that at least one harmonic of said pulse width modulated signal is outside a selected frequency band including said signal received by said radio receiver.

16. A switched mode power supply for use in a system including a radio receiver comprising:

a transistor for driving an output; and

circuitry for generating a pulse width modulated signal for switching said transistor on and off at a switching frequency selected as a function of a reception frequency of said radio receiver.

17. The power supply of Claim 16 wherein said switching frequency is selected such that at least one harmonic of said switching frequency is outside a selected frequency band including said signal received by said radio receiver.

18. The power supply of Claim 16 wherein said circuitry for generating comprises:

a crystal oscillator for generating said switching frequency using a selected one of a plurality of crystals of differing resonance frequencies; and

circuitry for selecting the one of the plurality of crystals for generating said switching frequency as a function of a frequency of said reception frequency.

19. The power supply of Claim 18 wherein said circuitry for selecting comprises a microcontroller.

20. The power supply of Claim 16 wherein said circuitry for generating comprises:

a base frequency generator; and

a programmable divider for dividing said base frequency by a selected divisor to generate said switching frequency.

21. A method of switching a power transistor used in a radio receiver comprising the steps of:

determining a frequency of a received signal being received by the radio receiver; and

generating a switching signal for switching the power transistor in response to said step of determining, a frequency of the switching signal selected such that at least one harmonic of the switching signal is outside a frequency band including the frequency of the received signal.

22. The method of Claim 21 wherein the radio includes a local oscillator and said step of determining comprises the step of counting periods of the local oscillator.

23. The method of Claim 21 wherein the radio includes a microcontroller and said step of determining comprises the step of decoding user input selecting the frequency of the received signal.

24. The method of Claim 21 wherein said step of generating comprises the substeps of:

selecting a crystal from a plurality of crystals of differing resonance frequencies; and

generating the frequency of the switching signal from the selected crystal using a crystal oscillator.

25. The method of Claim 21 wherein said step of generating comprises the substeps of:

generating a base frequency; and

dividing the base frequency by a selected factor to generate the switching frequency.